

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. SAKAI, et al
Serial No.: Not Yet Assigned
Filed: On Even Date
For: METHOD OF PRODUCING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

December 29, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Although some of the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98 (a) (3) for a concise explanation of the relevance is satisfied by the attached English language abstracts, English language version or translations of the documents and/or the discussion of the documents in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Mattingly, Stanger & Malur, P.C. Deposit Account No. 50-1417 (Case: T&A-135), and please credit any excess fees to such deposit account.

Respectfully submitted,

Mattingly, Stanger & Malur, P.C.

John R. Mattingly
Registration No. 30,293

JRM/nac

Attachments

10/519799
DT12 Rec'd PCT/PTO 29 DEC 2004
PTO/CB/2004-100-2004

PTO/SB/08a (08-03)

Approved through 07/31/2006. OMB 0651-0031
GSA FPMR (41 CFR) 101-11.5

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known	
Application Number	NOT YET ASSIGNED
Filing Date	December 29, 2004
First Named Inventor	Satoshi SAKAI, et al
Art Unit	
Examiner Name	

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Attorney Docket Number T&A-135

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ , Number ⁴ , Kind Code ⁵ (if known)				
		WO 03/049188 A1	06-12-2003			
		JP 2000-022007	01-21-2000	Fujitsu Ltd		
		JP-2003-158195 A	05-30-2003			
		JP-2003-0092233-A1	05-15-2003			

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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